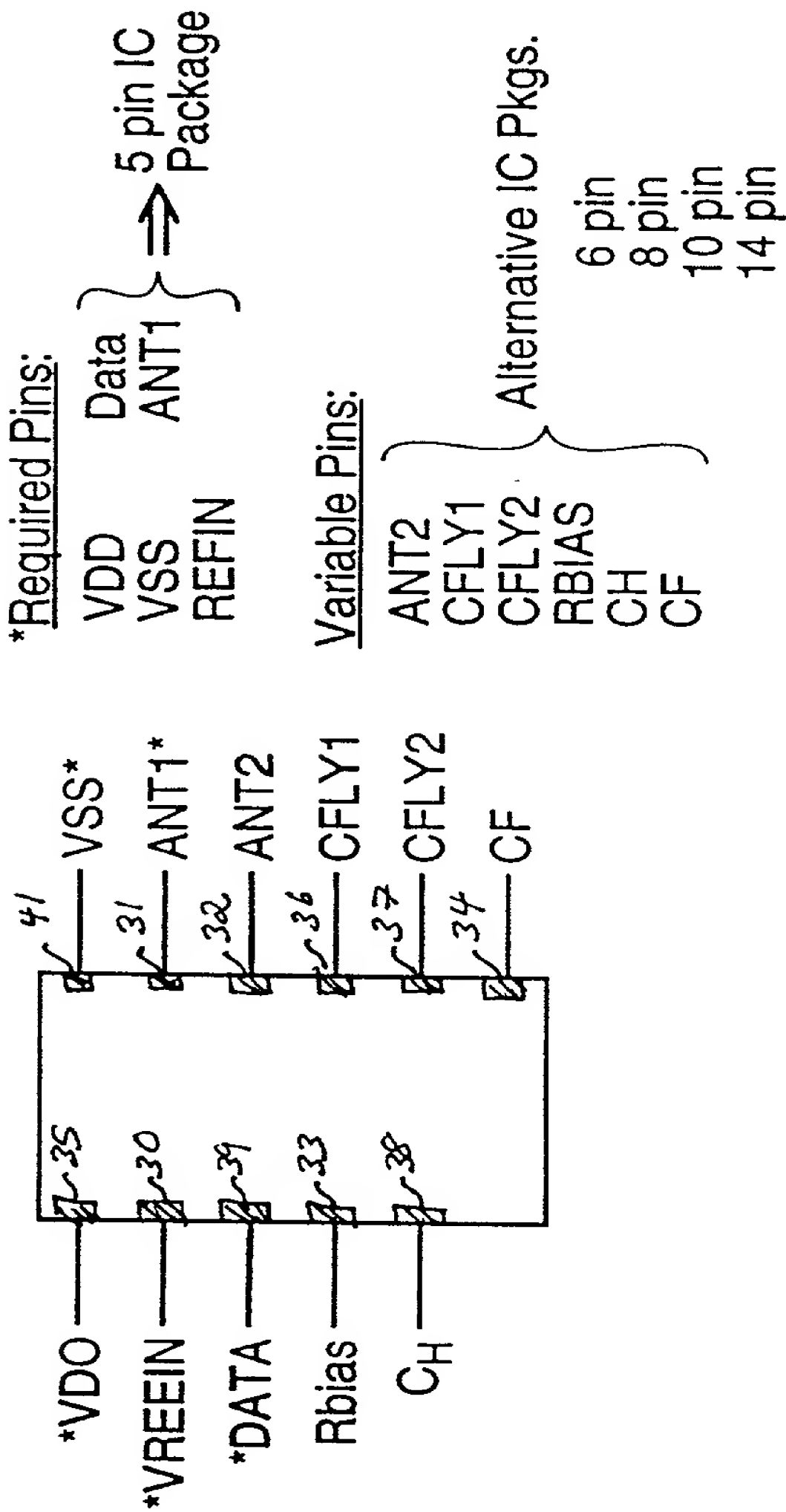




FIG. 1a



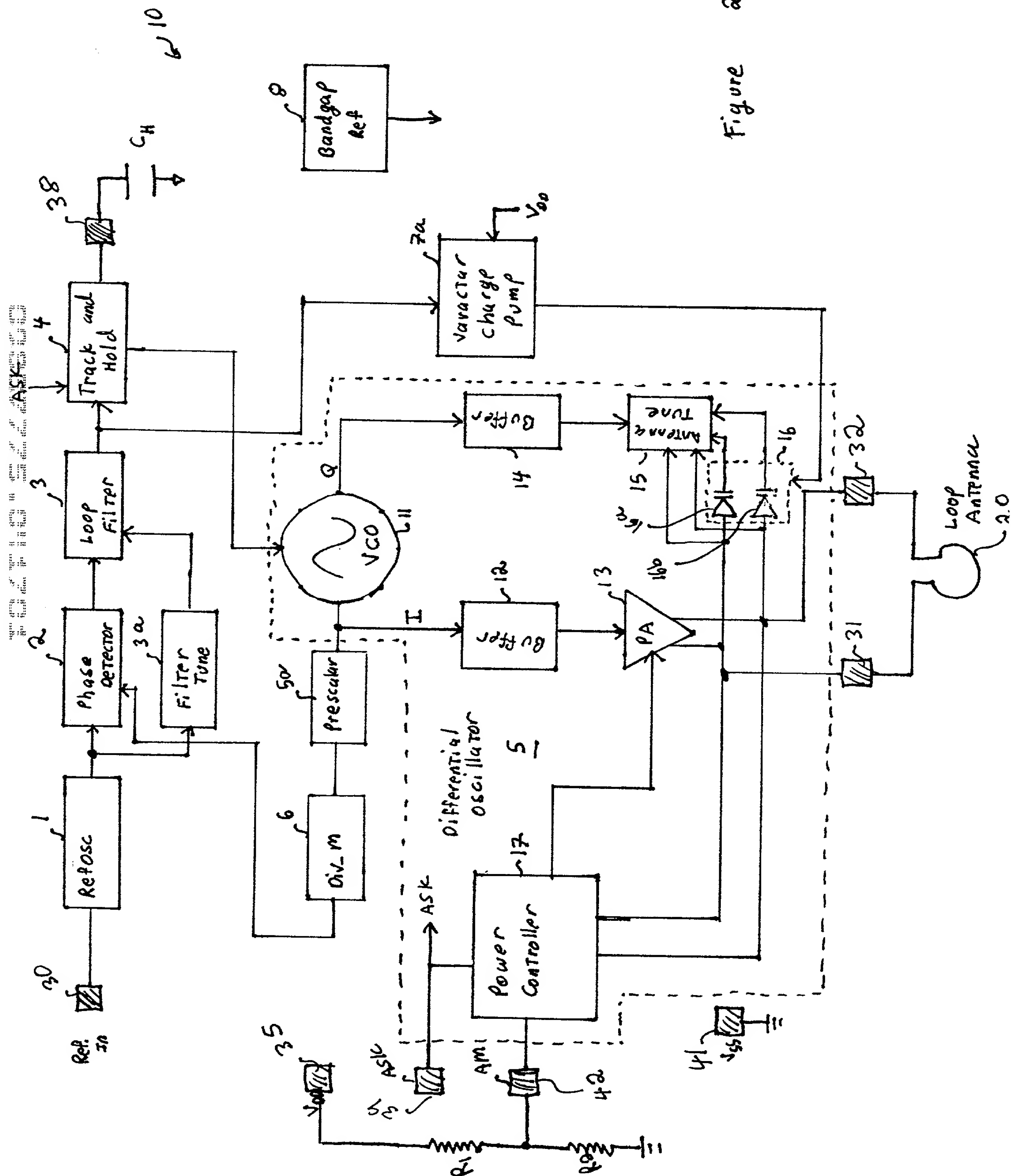


Figure 2a

CONNECTION PIN DIAGRAM FOR THE INVENTION  
FULLY INTEGRATED, ALL CMOS ASK/AM TRANSMITTER WITH AUTOMATIC ANTENNA TUNING

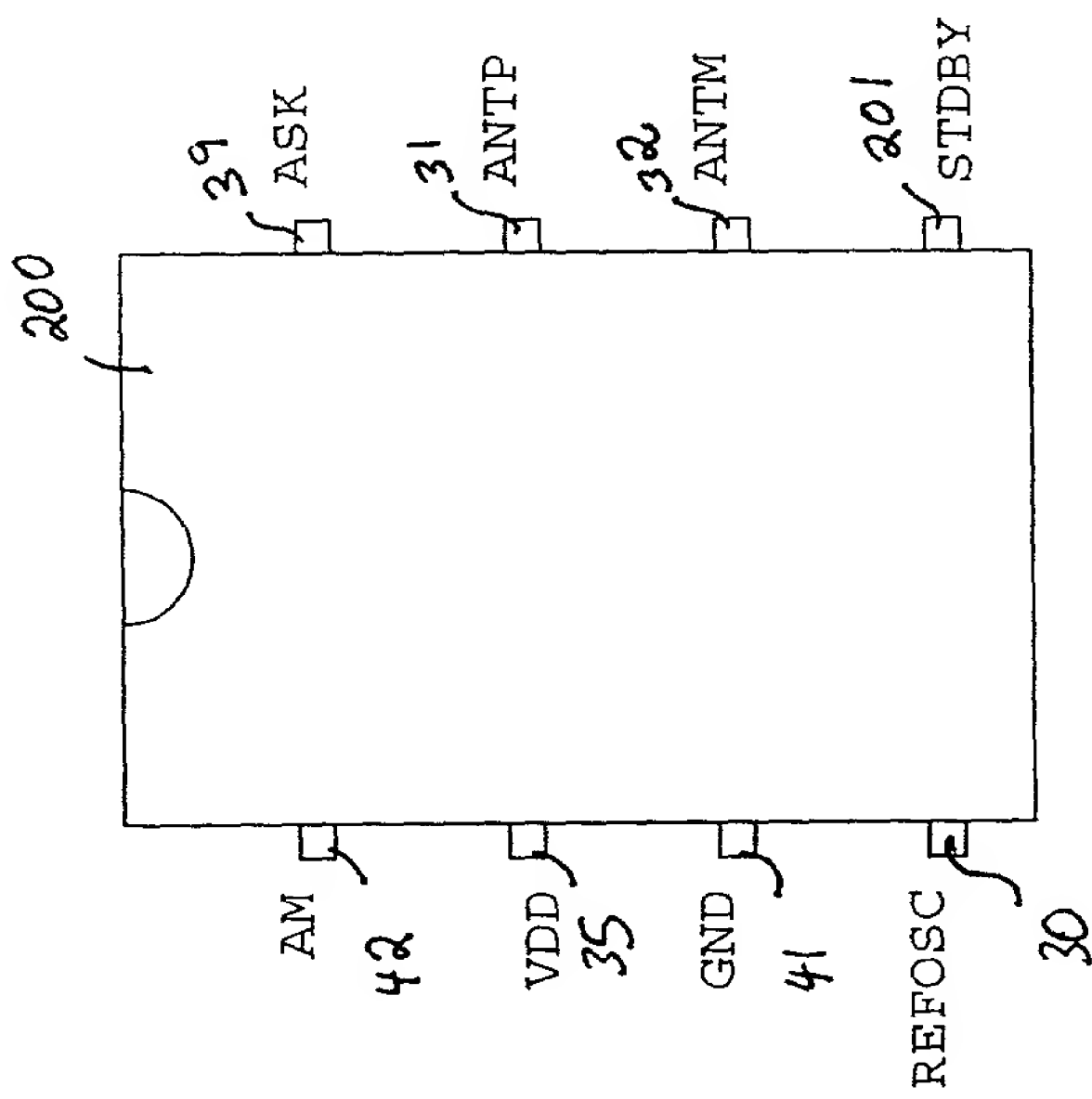


FIGURE 2b

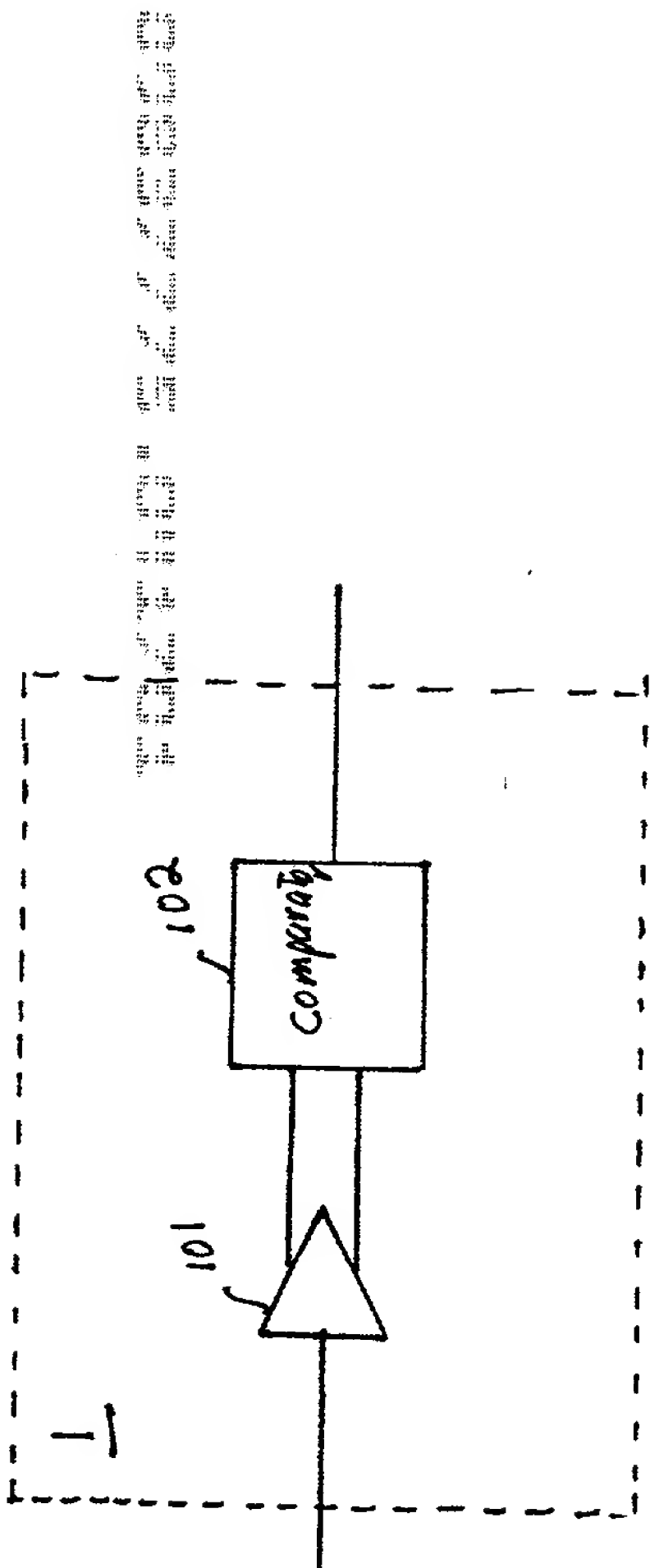


Figure 3a

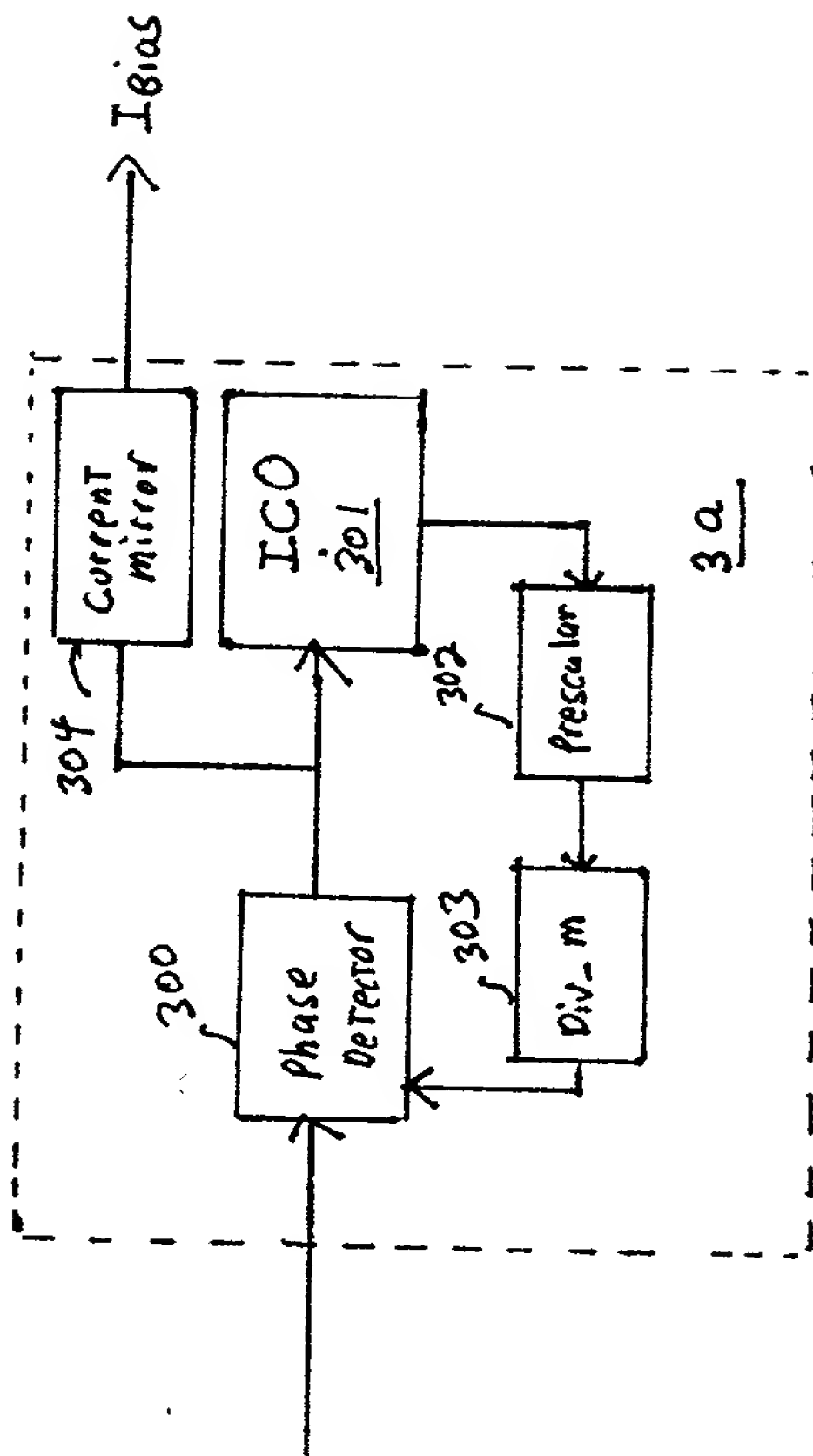


Figure 3b

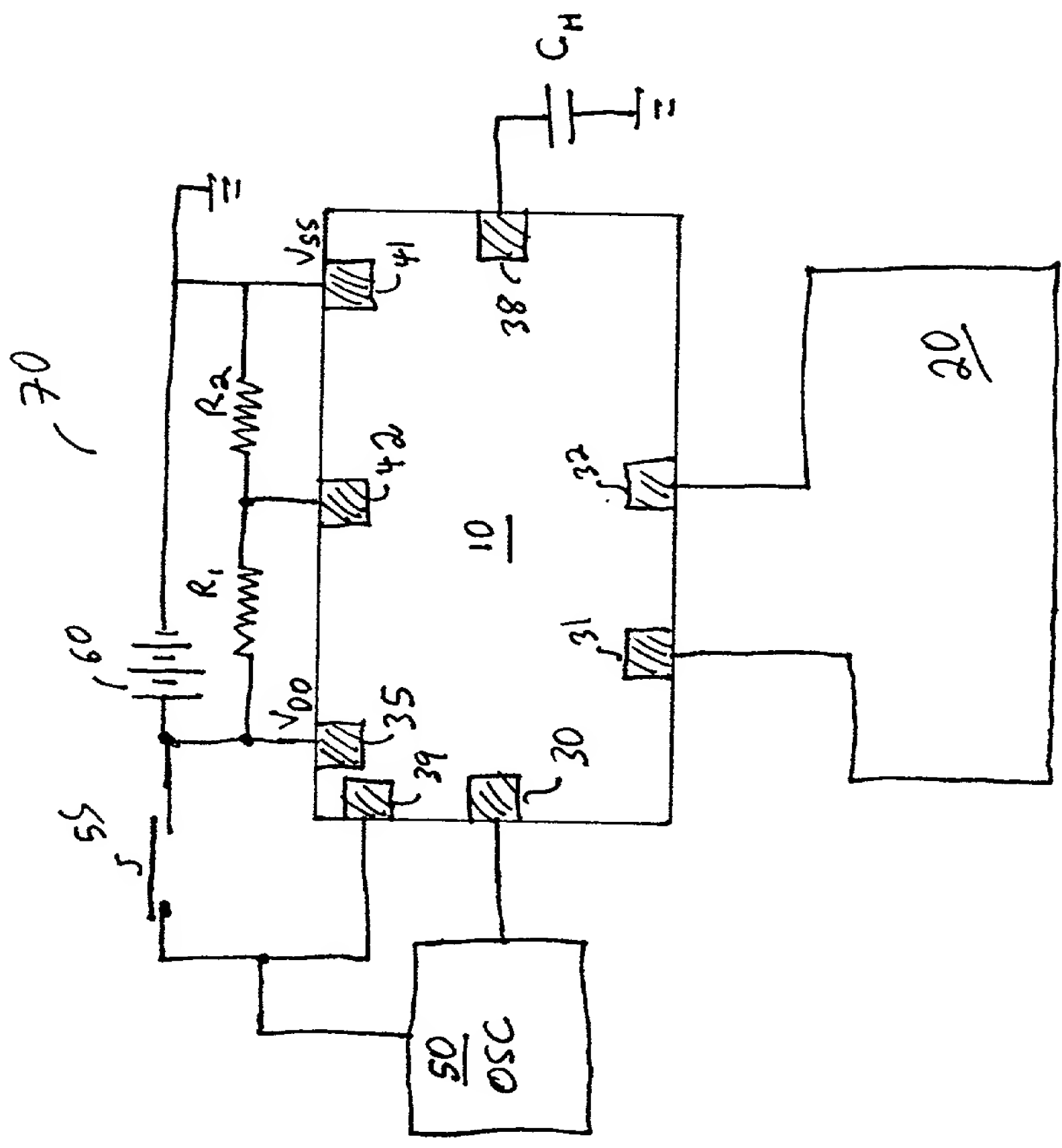


Figure 4

# AUTOMATIC ANTENNA TUNING

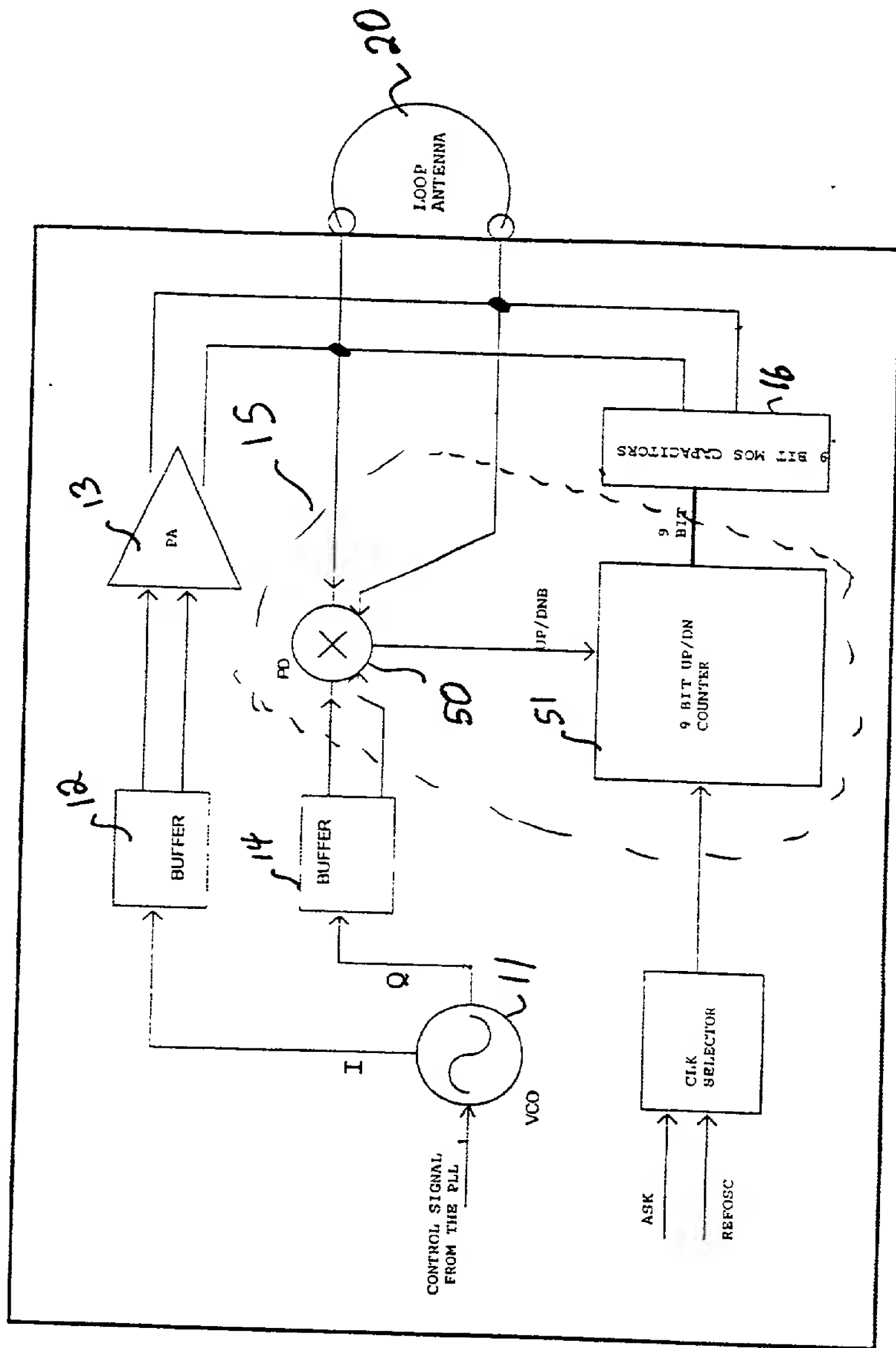


Figure 5a

CLOSED LOOP POWER CONTROL

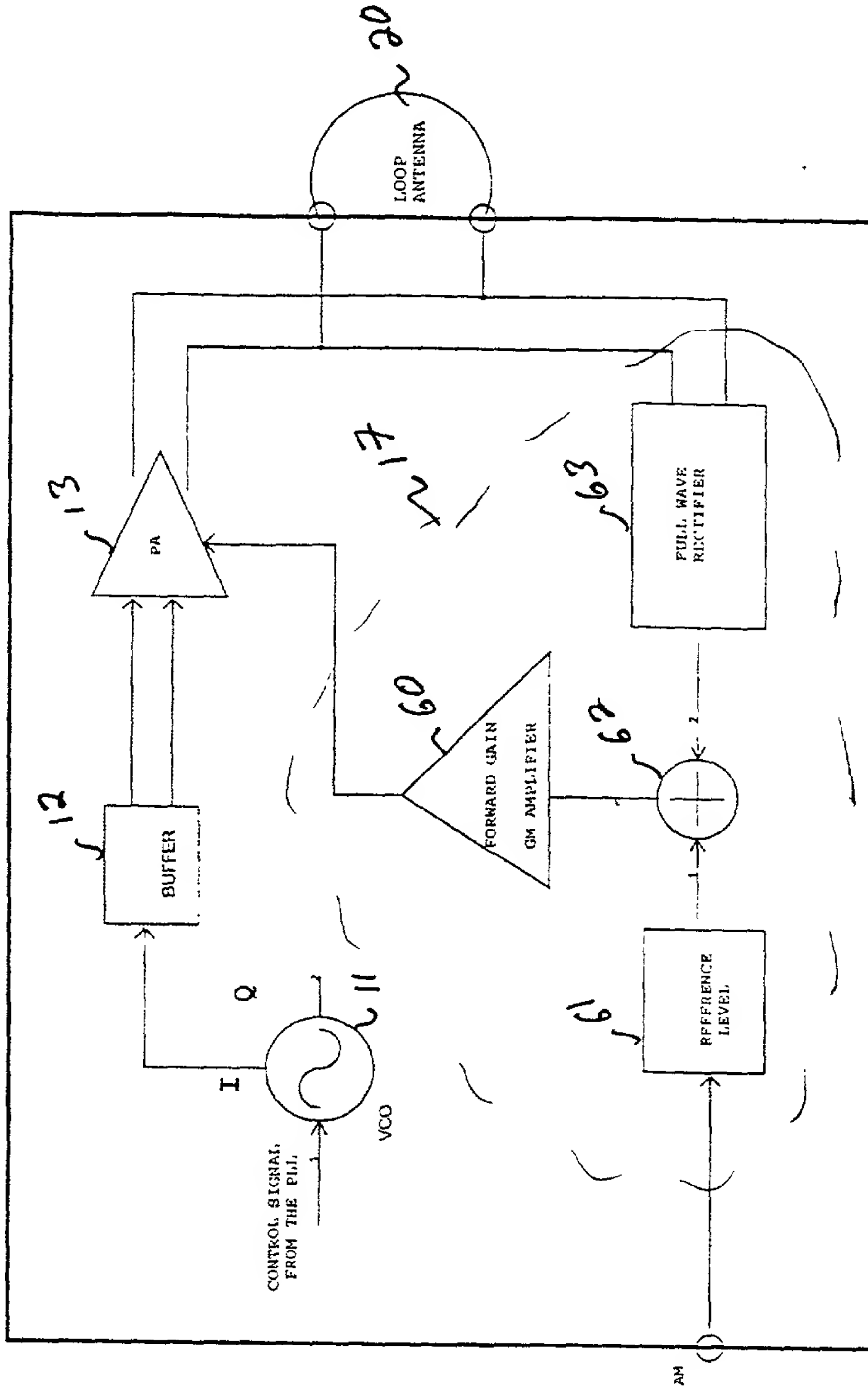


Figure 5b



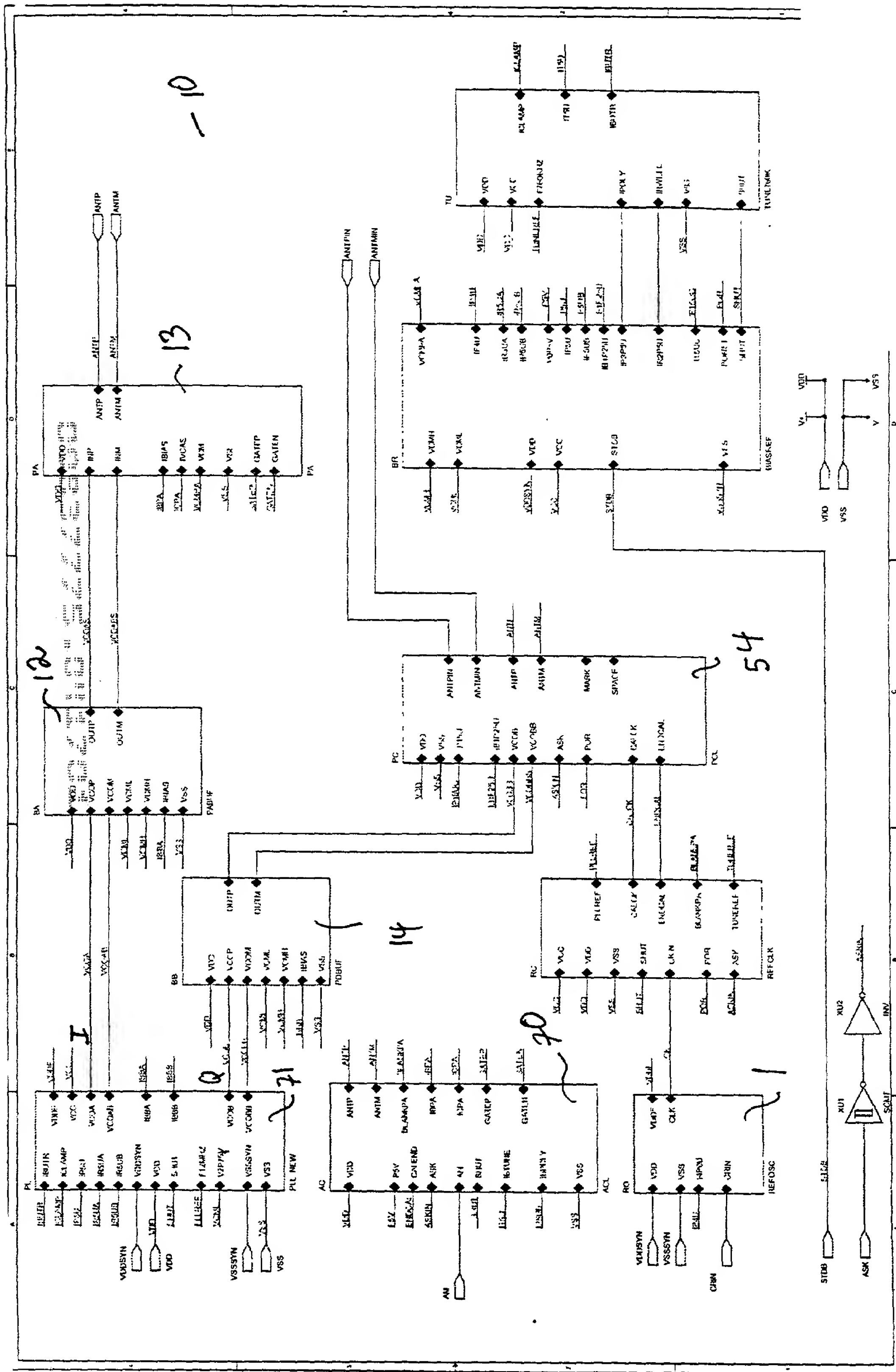


Figure 5C

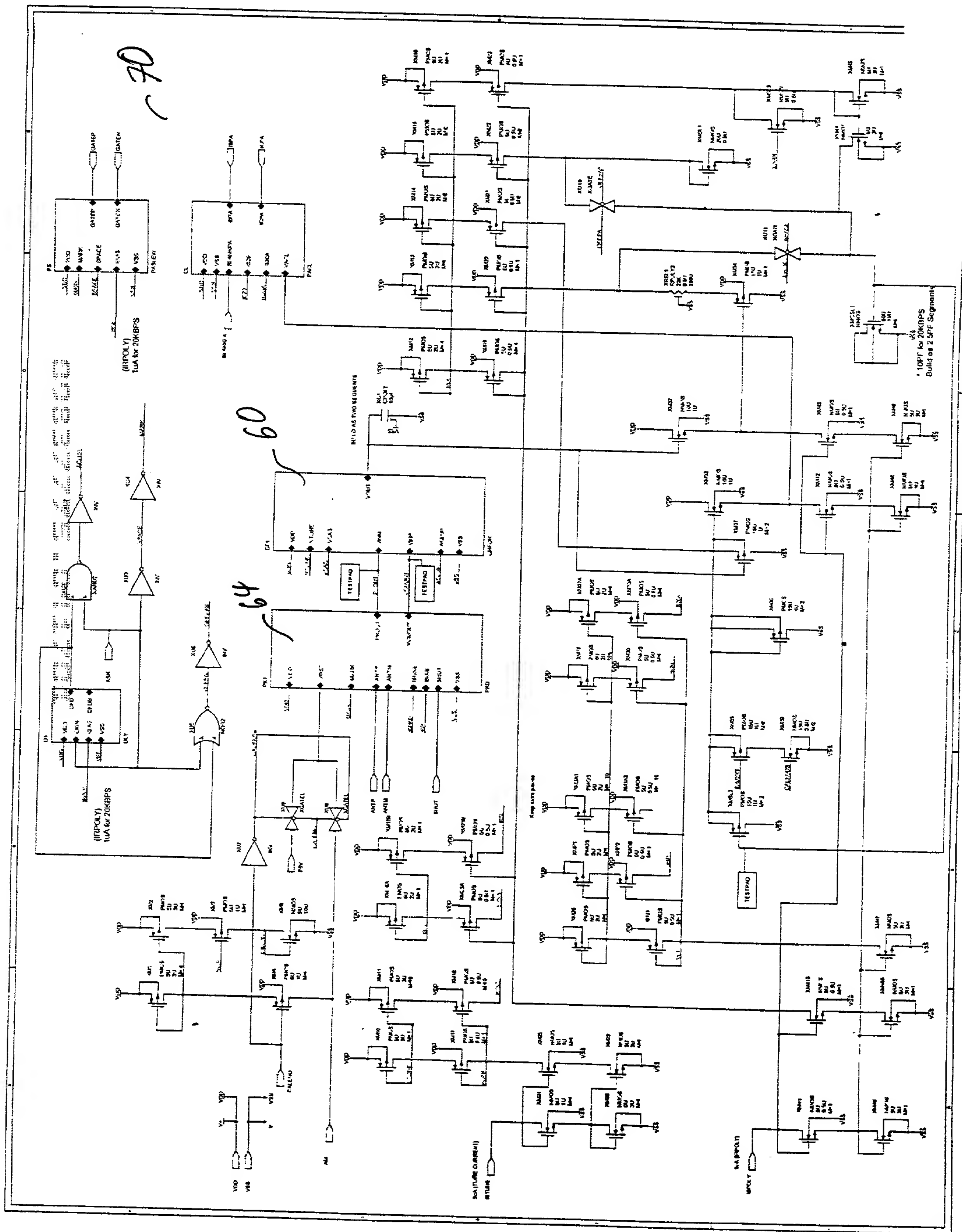


Figure 5d

64

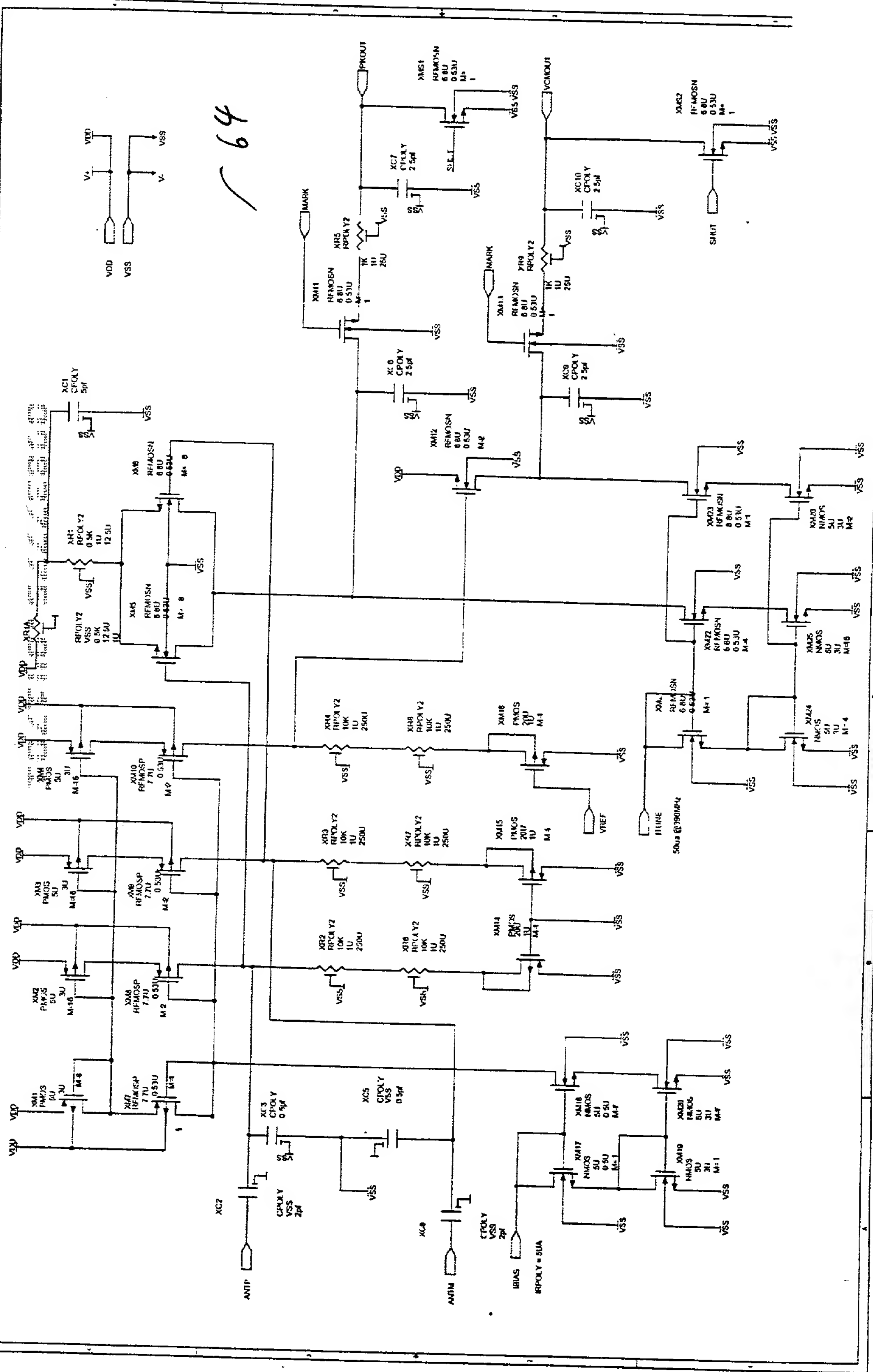


Figure 5c

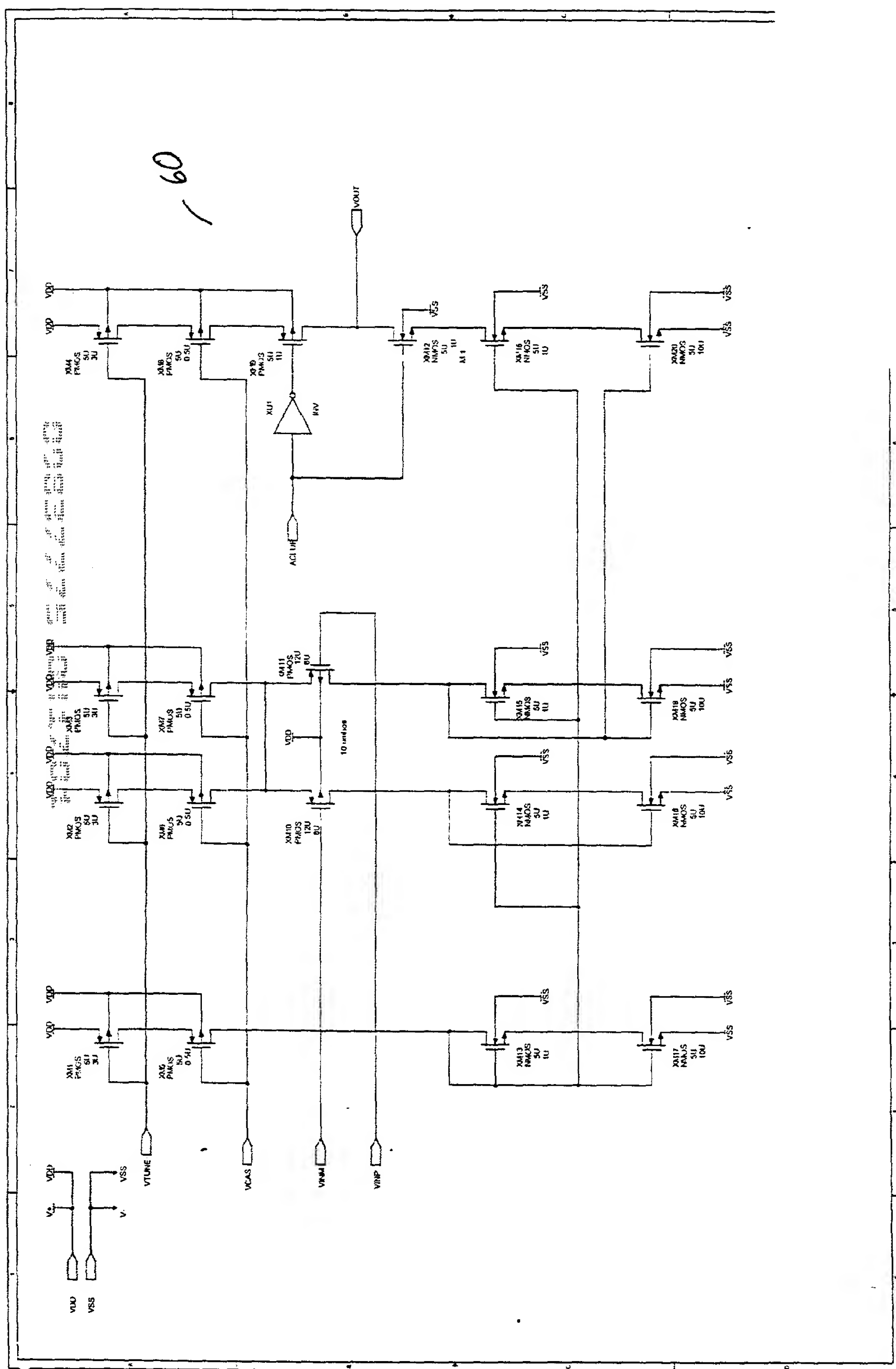


Figure 5A

[illegible]

Figure 5g

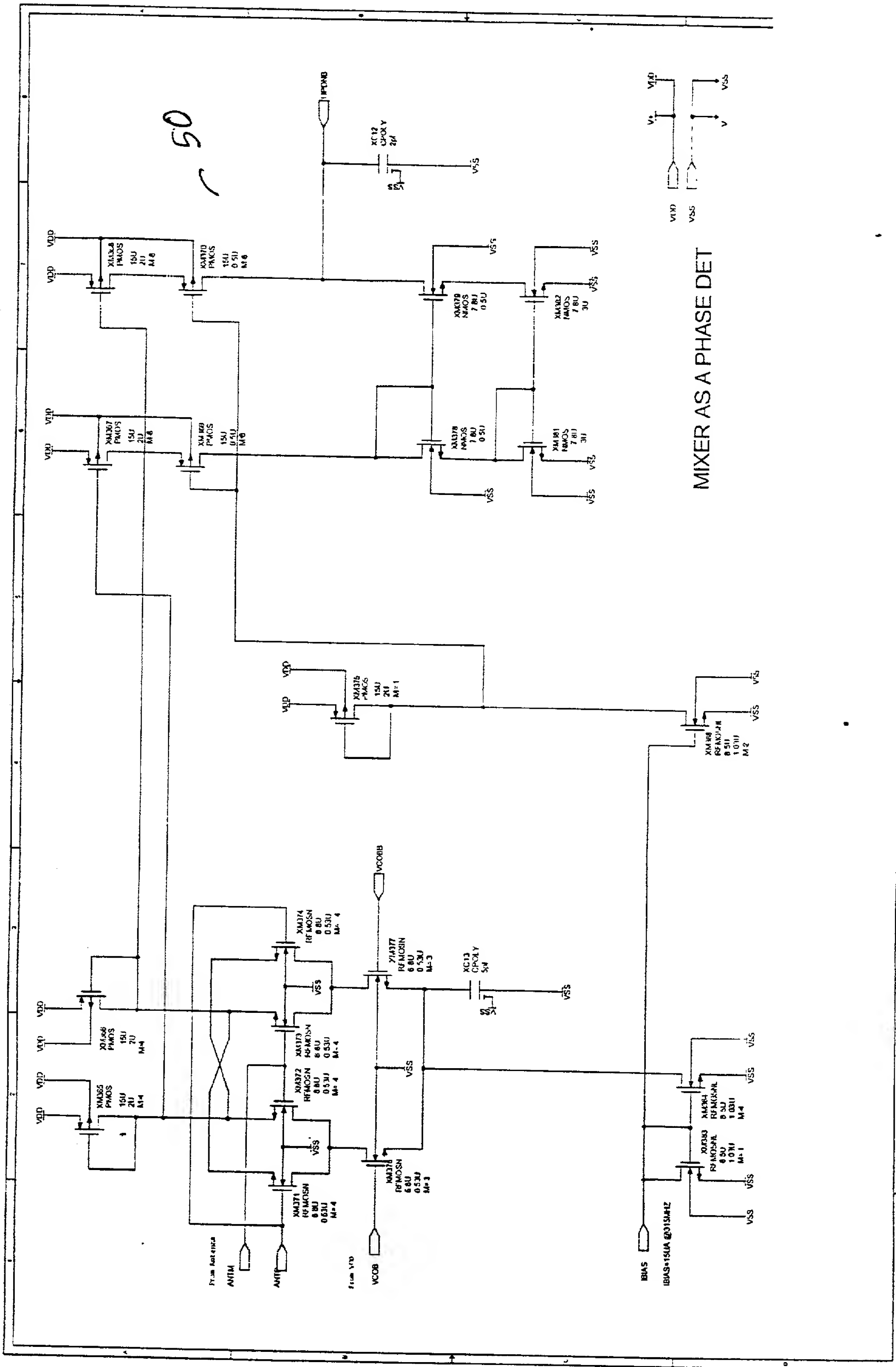


Figure 50

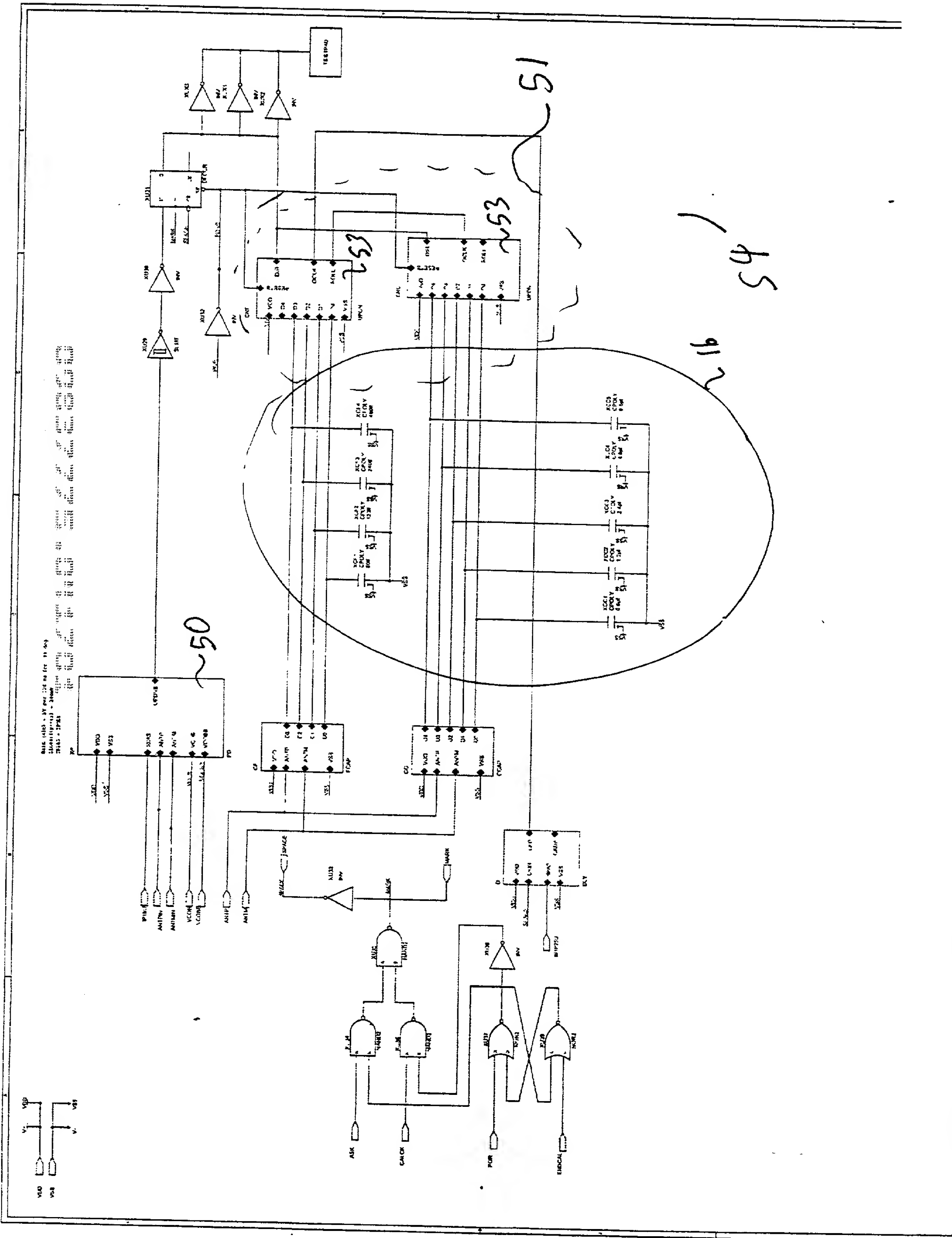
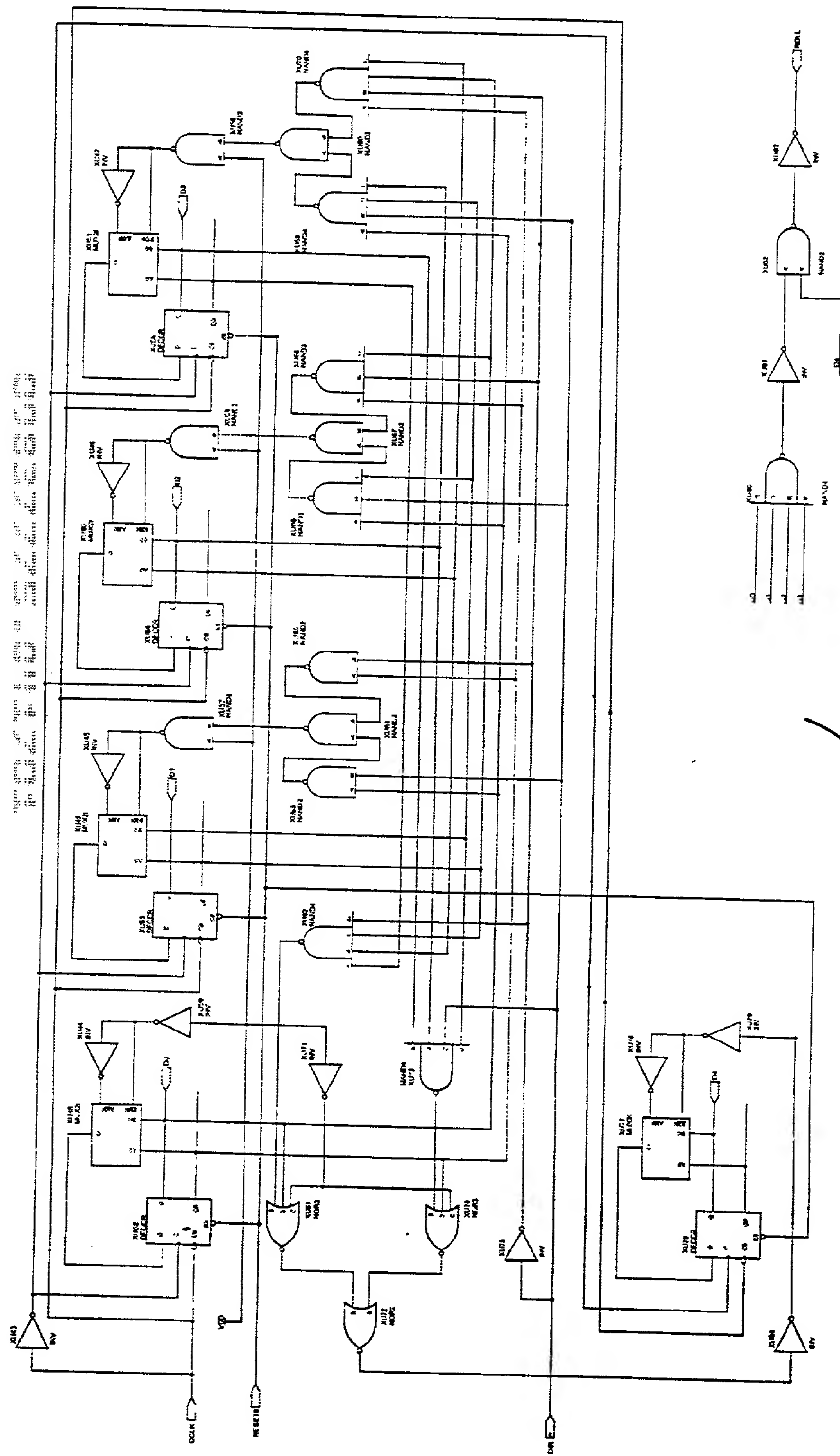


Figure 5A



5 BIT UP/DOWN COUNTER

Figure 5j